**Подключение периферии микроконтроллера на стенде ЭГУРМ**

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|  | Функционал | Периферия | Периферия микроконтроллера | Порт | Ножка микрокон-троллера | Примеча-ние |
| 1 | Revers | PORT | GPIO\_OUT | PB14 | (35) | Default |
| 2 | Uпит | ADC | ADC0\_IN10 | PC0 | (8) | Alternat 2 |
| 3 | Curent | ADC | ADC0\_IN11 | PC1 | (9) | Alternat 2 |
| 4 | PWM\_Mufta | TIM\_CHx | TIMER4\_CH1 | PA1 | (15) | Alternat 2 |
| 5 | CAN\_Rx | CAN\_Rx | CAN1\_Rx | PB12 | (33) | Alternat 2 |
| 6 | CAN\_Tx | CAN\_Tx | CAN1\_Tx | PB13 | (34) | Alternat 2 |
| 7 | USB\_DM | USBDM | USBDM | PA11 | (44) | Alternat 2 |
| 8 | USB\_DP | USBDP | USBDP | PA12 | (45) | Alternat 2 |
| 9 | ENC\_A | TIM\_CH0 | TIMER2\_CH0 | PB4 | (56) | Alternat 2 |
| 10 | ENC\_B | TIM\_CH1 | TIMER2\_CH1 | PB5 | (57) | Alternat 2 |
| 11 | ENC\_R | TIM\_CHx | TIMER9\_CH0 | PB8 | (61) | Alternat 2 |
| 12 | Moment | ADC | ADC0\_IN12 | PC2 | (10) | Alternat 2 |
| 13 | ENC\_A | TIM\_CH0 | TIMER3\_CH0 | PB6 | (58) | Alternat 2 |
| 14 | ENC\_B | TIM\_CH1 | TIMER3\_CH1 | PB7 | (59) | Alternat 2 |
| 15 | ENC\_R | TIM\_CHx | TIMER10\_CH0 | PB9 | (62) | Alternat 2 |
| 16 | Moment | ADC | ADC0\_IN13 | PC3 | (11) | Alternat 2 |
| 17 | АКИП-1148А-80-120 (RS232\_Tx) | USART\_Tx | USART2\_Tx | PB10 | (29) | Alternat 2 |
| 18 | АКИП-1148А-80-120 (RS323\_Rx) | USART\_Rx | USART2\_Rx | PB11 | (30) | Alternat 2 |
| 19 | MP-J2S-700A (RS232\_Tx) | USART\_Tx | USART1\_Tx | PA2 | (16) | Alternat 2 |
| 20 | MP-J2S-700A (RS323\_Rx) | USART\_Rx | USART1\_Rx | PA3 | (17) | Alternat 2 |
| 21 | SWDIO | SWDIO | SWDIO | PA13 | (46) | Default |
| 22 | SWCLK | SWCLK | SWCLK | PA14 | (49) | Default |
| 23 | Зажигание |  |  |  |  | Default |
| 24 | Период 10 мкс | TIMx | TIMER5 | --- | --- |  |
| 25 | Период 100 мкс | TIMx | TIMER6 | --- | --- |  |
| 26 | ADC0\_DMA | DMAx | DMA0\_CH0 | --- | --- |  |
| 27 | USART2\_Tx\_DMA | DMAx | DMA0\_CH1 | --- | --- | АКИП-1148А |
| 28 | USART2\_Rx\_DMA | DMAx | DMA0\_CH2 | --- | --- | АКИП-1148А |
| 29 | Датчик температуры | ADC | ADC0\_IN14 | PC4 | (24) | Alternat 2 |
| 30 | USART1\_Tx\_DMA | DMAx | DMA0\_CH6 | --- | --- | MP-J2S-700A |
| 31 | USART1\_Rx\_DMA | DMAx | DMA0\_CH5 | --- | --- | MP-J2S-700A |
| 32 |  |  |  |  |  |  |
| 33 |  |  |  |  |  |  |
| 34 |  |  |  |  |  |  |
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